


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)
Search: ☒ The ACM Digital Library ☐ The Guide


 Searching within **The ACM Digital Library** for: code offline online cache ([start a new search](#))

 Found **515** of **277,885**
REFINE YOUR SEARCH
[Search Results](#)
[Related Journals](#)
[Related Magazines](#)
[Related SI](#)

Sort by

▼ [Refine by Keywords](#)

[Discovered Terms](#)

▼ [Refine by People](#)

[Names](#)

[Institutions](#)

[Authors](#)

[Editors](#)

[Reviewers](#)

▼ [Refine by Publications](#)

[Publication Year](#)

[Publication Names](#)

[ACM Publications](#)

[All Publications](#)

[Content Formats](#)

[Publishers](#)

▼ [Refine by Conferences](#)

[Sponsors](#)

[Events](#)

[Proceeding Series](#)

Results 1 - 20 of 515

[Save results to a Binder](#)

Result page:

1 [Fast online pointer analysis](#)
[Martin Hirzel, Daniel Von Dincklage, Amer Diwan, Michael Hind](#)

April 2007

Transactions on Programming Languages and Systems
Publisher: ACM [Request Permissions](#)

 Full text available: [Pdf](#) (430.96 KB)

 Additional Information: [full citation](#), [abs](#)
Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 120, Downl

Pointer analysis benefits many useful clients, such as compiler optimization. However, analyzing common programming language features such as dynamic loading, reflection, and pointer analysis difficult. ...

Keywords: Pointer analysis, class loading, native interface, reflection

2 [Reducing the harmful effects of last-level cache polluters with an OS](#)
[Livio Soares, David Tam, Michael Stumm](#)

 November 2008 **MICRO '08: Proceedings of the 2008 41st IEEE/ACM International Symposium on Microarchitectures**
Publisher: IEEE Computer Society

 Full text available: [Pdf](#) (681.23 KB)

 Additional Information: [full citation](#), [abs](#)
Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 152, Downlo

It is well recognized that LRU cache-line replacement can be ineffective for localized memory access patterns. Specifically, in last-level processor caches, inserting non-reuseable ...

3 [LLVM: A Compilation Framework for Lifelong Program Analysis & Transformation](#)
[Chris Lattner, Vikram Adve](#)

 March 2004 **CGO '04: Proceedings of the international symposium on Compiler construction, directed and runtime optimization**
Publisher: IEEE Computer Society

 Full text available: [Pdf](#) (256.90 KB)

 Additional Information: [full citation](#), [abs](#)
Bibliometrics: Downloads (6 Weeks): 44, Downloads (12 Months): 286, Downl

This paper describes LLVM (Low Level Virtual Machine), a compiler framework for lifelong program analysis and transformation for arbitrary programs, by performing compiler transformations at compile-time, ...

4 [Dynamic Class Hierarchy Mutation](#)
ADVANCED SEARCH
[Advanced Search](#)
FEEDBACK
[Please provide us with feedback](#)

 Found **515** of **277,885**

[Lixin Su](#), [Mikko H. Lipasti](#)

March 2006 **CGO '06**: Proceedings of the International Symposium on Co

Publisher: IEEE Computer Society

Full text available:  [Pdf](#) (377.30 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 19, Download

Class hierarchies in object-oriented programs are used to capture various
represent, allowing programmers to encapsulate common attributes in
in lower-level ...

5 [A multi-version cache replacement and prefetching policy for hybrid](#)

[André Seifert](#), [Marc H. Scholl](#)

August 2002 **VLDB '02**: Proceedings of the 28th international conference

Publisher: VLDB Endowment


Full text available:  [Pdf](#) (202.49 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 21, Download


This paper introduces MICO, a novel multiversion integrated cache replacement
efficient cache and transaction management in hybrid data delivery networks
and sporadically ...

6 [Optimal multistream sequential prefetching in a shared cache](#)

 [Binny S. Gill](#), [Luis Angel D. Bathen](#)

October 2007 **Transactions on Storage (TOS)**, Volume 3 Issue 3

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (1.04 MB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 110, Download

Prefetching is a widely used technique in modern data storage systems.
prefetching algorithms known as *sequential prefetching*. There are two
sequential prefetching ...

Keywords: Adaptive prefetching, asynchronous prefetching, cache polling
multistream read, optimal prefetching, prefetch wastage, prestaging, sequential
trigger distance

7 [New results on web caching with request reordering](#)

 [Susanne Albers](#)

June 2004 **SPAA '04**: Proceedings of the sixteenth annual ACM symposium on
architectures

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (186.52 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 19, Download

We study web caching with request reordering. The goal is to maintain a set of
requests can be served at low cost. To improve cache hit rates, a limiter
[6], who recently ...

Keywords: approximation, batch, cache, competitive, document, offline

8 [LLVA: A Low-level Virtual Instruction Set Architecture](#)

Vikram Adve, Chris Lattner, Michael Brukman, Anand Shukla, Brian Gaeke
December 2003 **MICRO 36:** Proceedings of the 36th annual IEEE/ACM International Symposium on Microarchitecture

Publisher: IEEE Computer Society


Full text available:  [Pdf](#) (196.08 KB)

Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 92, Downloads (All Time): 1,234

A virtual instruction set architecture (V-ISA) implemented via a processor core provides great flexibility to processor designers. Recent examples such as Crusoe hardware instruction ...


9 [Mostly static program partitioning of binary executables](#)

 Efe Yardimci, Michael Franz

June 2009

Transactions on Programming Languages and Systems

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (2.23 MB)


Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 32, Downloads (12 Months): 263, Downloads (All Time): 1,234

We have built a runtime compilation system that takes unmodified sequential code on off-the-shelf multiprocessors using dynamic vectorization and loop-level parallelism. Azure, is purely software ...

Keywords: Continuous compilation and optimization, binary translation, runtime compilation

10 [Phase-based adaptive recompilation in a JVM](#)

 Dayong Gu, Clark Verbrugge

April 2008

CGO '08: Proceedings of the sixth annual IEEE/ACM international conference on Compiler construction and optimization

Publisher: ACM

Full text available:  [Pdf](#) (431.13 KB)

Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 63, Downloads (All Time): 1,234

Modern JIT compilers often employ multi-level recompilation strategies to optimize code also the most highly optimized, balancing optimization costs and expected execution time to compile and ...

Keywords: adaptive optimization, hardware counters, java, runtime testing


11 [System synthesis of synchronous multimedia applications](#)

 Gang Qu, Miodrag Potkonjak

February 2003

Transactions on Embedded Computing Systems (TECS)

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (286.29 KB)


Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 76, Downloads (All Time): 1,234


Modern system design is being increasingly driven by applications such as multimedia processing, embedded systems, and ...

communications, which have intrinsic quality of service (QoS) requirements. One of the most ...

Keywords: high-level embedded systems synthesis, on-chip memory n

- 12** [The quest for scalable support of data-intensive workloads in distributed systems](#)
 Ioan Raicu, Ian T. Foster, Yong Zhao, Philip Little, Christopher M. Moretti, /
 June 2009 **HPDC '09: Proceedings of the 18th ACM international symposium on distributed computing**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (1.28 MB)

Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 217, Downloads (24 Months): 100

Data-intensive applications involving the analysis of large datasets offer challenges, for which data locality can be crucial to high throughput and energy efficiency. This paper proposes a data-aware scheduling approach that ...

Keywords: data diffusion, data management, data-aware scheduling, f

- 13** [Java object header elimination for reduced memory consumption in the JVM](#)
 Kris Venstermans, Lieven Eeckhout, Koen De Bosschere
 September 2007 **Transactions on Architecture and Code Optimization**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (722.38 KB)


Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 78, Downloads (24 Months): 100



Memory performance is an important design issue for contemporary computer systems due to the increasing processor/memory speed gap. This paper proposes a space-efficient Java virtual machine (JVM) that reduces the memory consumption of 64-bit Java virtual machines. ...

Keywords: 64-bit implementation, Java object model, Virtual machine, memory performance

- 14** [Caching XML Web Services for Mobility](#)


 May 2003 **Queue**, Volume 1 Issue 3

Publisher: ACM  [Request Permissions](#)

Full text available:  Html (35.15 KB),  Pdf (311.20 KB) Additional Information: [full citation](#), [abstract](#)


Bibliometrics: Downloads (6 Weeks): 149, Downloads (12 Months): 1229, Downloads (24 Months): 100

- 15** [Simulation-based verification using Temporally Attributed Boolean Logic](#)

 S. K. Panda, Arnab Roy, P. P. Chakrabarti, Rajeev Kumar

September 2008 **Transactions on Design Automation of Electronic Systems**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (1.49 MB)

Additional Information: [full citation](#), [abstract](#)


Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 63, Downloads (24 Months): 100

We propose a specification logic called Temporally Attributed Boolean (TAB) which allows us to: (i) represent assertions succinctly, (ii) incorporate d

design intentions. ...

Keywords: Bus verification, instruction semantics verification, interrupt simulation based verification, temporal logic, timing verification

16 [A two-phase escape analysis for parallel java programs](#)

 [Kyungwoo Lee, Samuel P. Midkiff](#)

September 2006 **PACT '06:** Proceedings of the 15th international conference on parallel architectures and techniques

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (288.22 KB)


Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 40, Downloads (All Time): 100

Thread escape analysis conservatively determines which objects may be accessed by a thread. Thread escape analysis is useful for a variety of purposes—finding races in multithreaded programs, detecting synchronization errors, allocating memory for garbage collection, etc.

Keywords: compilation models, consistency models, escape analysis, java

17 [HMTT: a platform independent full-system memory trace monitoring](#)

 [Yungang Bao, Mingyu Chen, Yuan Ruan, Li Liu, Jianping Fan, Qingbo Yuan,](#)

June 2008 **SIGMETRICS '08:** Proceedings of the 2008 ACM SIGMETRICS conference on data-driven modeling of computer systems

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (984.65 KB)


Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 14, Downloads (12 Months): 128, Downloads (All Time): 250

Memory trace analysis is an important technology for architecture research, system optimization, and application performance improvements. Many approaches have been proposed, such as simulation, binary instrumentation, etc.

Keywords: DIMM, HMTT, memory trace, real system

18 [Online expansion of rare queries for sponsored search](#)

 [Andrei Broder, Peter Ciccolo, Evgeniy Gabrilovich, Vanja Josifovski, Donald](#)

April 2009 **WWW '09:** Proceedings of the 18th international conference on the World Wide Web

Publisher: ACM

Full text available:  [Pdf](#) (785.09 KB)




Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 213, Downloads (All Time): 450

Sponsored search systems are tasked with matching queries to relevant documents. Existing matching algorithms expand the user's query using a variety of external information, such as these expansion-based algorithms.




Keywords: query expansion, sponsored search, tail queries

19 [Multiprocessor performance estimation using hybrid simulation](#)

-  [Lei Gao, Kingshuk Karuri, Stefan Kraemer, Rainer Leupers, Gerd Ascheid, f](#)
 June 2008 **DAC '08**: Proceedings of the 45th annual Design Automation
Publisher: ACM  [Request Permissions](#)
 Full text available:  [Pdf](#) (858.95 KB) Additional Information: [full citation](#), [abstract](#)
Bibliometrics: Downloads (6 Weeks): 21, Downloads (12 Months): 128, Downl

With the growing number of programmable processing elements in toda designs, the synergy required for the development of the hardware arch also increasing. In ...

Keywords: HySim, MPSoC, address recovery, cache simulation, cross r estimation

- 20** [Categorizing web search results into meaningful and stable categori](#)
 [Bill Kules, Jack Kustanowitz, Ben Shneiderman](#)
 June 2006 **JCDL '06**: Proceedings of the 6th ACM/IEEE-CS joint confere
Publisher: ACM  [Request Permissions](#)
 Full text available:  [Pdf](#) (460.86 KB) Additional Information: [full citation](#), [abstract](#)
Bibliometrics: Downloads (6 Weeks): 19, Downloads (12 Months): 142, Downl

When search results against digital libraries and web resources have lim meaningful and stable category information can enable better overview: proposes six fast-feature techniques ...

Keywords: browsing, categorization, classification, metadata, open dir

Result page:

The ACM Portal is published by the Association for Computing Machinery. Copyright
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)